

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

<b>Applicant:</b>	Papadopoulou et al.	<b>Conf. No.:</b>	2038
<b>Serial No.:</b>	10/708,039	<b>Art Unit:</b>	2825
<b>Filing Date:</b>	02/04/2004	<b>Examiner:</b>	Parihar, Suchin
<b>Title:</b>	IC DESIGN MODELING ALLOWING DIMENSION-DEPENDENT RULE CHECKING	<b>Docket No.:</b>	BUR920030149US1 (IBMB-0034)

Mail Stop Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**AMENDMENT**

Sir:

**I. INTRODUCTORY COMMENTS:**

This paper is being filed in response to the Office Action dated January 8, 2007. Please reconsider the above-referenced patent application in view of the following amendments and remarks: